

3.2 (Part of) the Feasible Symbolic Tree

```

* | 1 [CallingRole=CallingRole] t ?TAddress@1 ?TSP@1
  [and(IsTCNreq(TSP@1),IsCallingOf(TAddress@1,TSP@1))]
  [IsTReq(TSP@1)] [730,744,766]
* | 1 i (enable: exit !TSP@1) [744]
* | | 1 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
bh1 * | | | 1 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%4,TSP@1)] i (specified explicitly) [798]
* | | | | 1 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
* | | | | | 1 [CalledRole=CalledRole] [CalledRole=CalledRole] t ?TAddress@6 !TSP%4.2
  [and(IsTCNind(TSP%4),IsCalledOf(TAddress@6,TSP%4))]
  [IsTInd(TSP%4)]
  [ne(TAddress@6,TAddress@1)] [730,746,783,788,798]
* | | | | | | 1 i (enable: exit !TSP%4) [746]
* | | | | | | | 1 i (enable: exit !TAddress@6) [788]
* | | | | | | | | 1 i (enable: exit !NoTReqs) [798]
* | | | | | | | | | 1 [Empty(NoTReqs)] t !TAddress@6 ?TSP@10
  [IsValidTCN2For(TSP@10,TSP%4)]
  [IsTReq(TSP@10)]
  [IsTReq(TSP@10)] [733,749,779,733,800]
* | | | | | | | | | | 1 [IsTEXOptionOf(TEXOption%11,TSP@10)] i (enable: exit !TEXOption%11) [751]
* | | | | | | | | | | | 1 i (enable: exit !TSP@10) [800]
* | | | | | | | | | | | | 1 [NonEmpty(TSP@10)] [IsIndicationOf(TSP%13,TSP@10)]
  i (specified explicitly) [798]
* | | | | | | | | | | | | | 1 t !TAddress@1 !TSP%13
  [IsValidTCN2For(TSP%13,TSP@1)]
  [IsTInd(TSP%13)] [733,749,783,733,798]
* | | | | | | | | | | | | | | 1 [IsTEXOptionOf(TEXOption%15,TSP%13)]
  i (enable: exit !TEXOption%15) [751]
* | | | | | | | | | | | | | | | 1 i (enable: exit !NoTReqs) [798]
* | | | | | | | | | | | | | | | | 1 [Empty(NoTReqs)] t !TAddress@1 ?TSP@17
  [IsTDF(TSP@17)]
  [IsTReq(TSP@17)] [IsTReq(TSP@17)] [733,758,779,733,800]
* | | | | | | | | | | | | | | | | | 1 i (enable: exit !TSP@17) [800]
* | | | | | | | | | | | | | | | | | | 1 [NonEmpty(TSP@17)] [IsIndicationOf(TSP%19,TSP@17)]
  i (specified explicitly) [798]
* | | | | | | | | | | | | | | | | | | | 1 t !TAddress@1 !TSP%19
  [IsTDF(TSP%19)]
  [IsTInd(TSP%19)] [733,758,783,733,798] => continue
... ..
* | | | 2 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786] => continue
* | | | 2 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%3,TSP@1)] i (specified explicitly) [798] => continue
* | | | 3 [CallingRole=CallingRole] i(enable: exit !TAddress@1) [786]
bh2 * | | | | 1 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786] => continue
* | | | | 2 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%4,TSP@1)] i (specified explicitly) [798] => continue
* | | | | 2 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
* | | | | | 1 i (enable: exit !TSP@1) [744] => again bh1
* | | | | 2 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%3,TSP@1)] i (specified explicitly) [798] => continue
* | | | | 3 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
bh3 * | | | | | 1 i (enable: exit !TSP@1) [744] => continue
* | | | | | 2 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%4,TSP@1)] i (specified explicitly) [798] => continue
* | | | | | 3 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%2,TSP@1)] i (specified explicitly) [798] => continue
* | | | | | 4 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
* | | | | | | 1 i (enable: exit !TSP@1) [744] => again bh2
* | | | | | 2 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786] => again bh3
* | | | | | 3 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%3,TSP@1)] i (specified explicitly) [798] => continue

```

The last part shows the complexity introduced by enables, since there are as many branches as there are possible permutations of successive enables. Note for example that [1.1.1] and [1.2.1] lead to the same behaviour labelled bh1.

Node [1.1.1.1.1] corresponds to node [1.1.1.1.9] in the tree of Annex 3.1. Its subtree was not explored there.

3 Process TConnection

3.0 Data Base of contradictions

- (1) IsTReq(@1) # IsTInd(@1)
- (2) IsTReq(@1) # IsTCONind(@1)
- (3) IsTReq(@1) # IsIndicationOf(@2,@1)
- (4) IsValidICON2For(@2,@1) & IsTReq(@1) # IsTReq(@2)
- (5) IsValidICON2For(@2,@1) & IsTInd(@1) # IsTInd(@2)
- (6) IsIndicationOf(@2,@1) & IsIDIS(@1) # IsIDF(@2)

3.1 (Part of) the Contextual Symbolic Tree

The applicable rules of section 2.2 are shown after reaching node [1.1.1.1].

```
1 [CallingRole=CallingRole] t ?TAddress@1 ?TSP@1
  [and(IsTCONreq(TSP@1),IsCallingOf(TAddress@1,TSP@1))]
  [IsTReq(TSP@1)] [730,744,766]
| 1 i (enable: exit !TSP@1) [744]
| | 1 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
| | | 1 [NonEmpty(TSP@1)] [IsIndicationOf(TSP%4,TSP@1)] i (specified explicitly) [798]
| | | | 1 [CallingRole=CallingRole] i (enable: exit !TAddress@1) [786]
| | | | 2 t !TAddress@1 !TSP%4
| | | | | [IsValidICON2For(TSP%4,TSP@1)]
| | | | | [IsTReq(TSP%4)] [733,750,779,733,798] ** rule C(4) **
| | | | 3 [CalledRole=CalledRole] t !TAddress@1 !TSP%4
| | | | | [IsValidICON2For(TSP%4,TSP@1)]
| | | | | [IsTInd(TSP%4)]
| | | | | [ne(TAddress@1,TAddress@1)] [733,750,783,788,798] ** rule A **
| | | | 4 [CalledRole=CalledRole] [Empty(NoTReqs)] t !TAddress@1 ?TSP@5
| | | | | [IsValidICON2For(TSP@5,TSP@1)]
| | | | | [IsTReq(TSP@5)]
| | | | | [ne(TAddress@1,TAddress@1)]
| | | | | [IsTReq(TSP@5)] [733,750,779,788,800] ** rule A **
| | | | 5 t !TAddress@1 !TSP%4
| | | | | [IsIDIS(TSP%4)]
| | | | | [IsTReq(TSP%4)] [733,764,779,733,798] ** rule C(3) **
| | | | 6 [CalledRole=CalledRole] t !TAddress@1 !TSP%4
| | | | | [IsIDIS(TSP%4)]
| | | | | [IsTInd(TSP%4)]
| | | | | [ne(TAddress@1,TAddress@1)] [733,764,783,788,798] ** rule A **
| | | | 7 [CalledRole=CalledRole] [Empty(NoTReqs)] t !TAddress@1 ?TSP@5
| | | | | [IsIDIS(TSP@5)]
| | | | | [IsTReq(TSP@5)]
| | | | | [ne(TAddress@1,TAddress@1)]
| | | | | [IsTReq(TSP@5)] [733,764,779,788,800] ** rule A **
| | | | 8 [CalledRole=CalledRole] t !TAddress@1 !TSP%4
| | | | | [and(IsTCONind(TSP%4),IsCalledOf(TAddress@1,TSP%4))]
| | | | | [IsTReq(TSP%4)] [730,746,779,733,798] ** rule B(2) **
| | | | 9 [CalledRole=CalledRole] [CalledRole=CalledRole] t ?TAddress@5 !TSP%4
| | | | | [and(IsTCONind(TSP%4),IsCalledOf(TAddress@5,TSP%4))]
| | | | | [IsTInd(TSP%4)]
| | | | | [ne(TAddress@5,TAddress@1)] [730,746,783,788,798] ==> continue
| | | | 10 [CalledRole=CalledRole] [CalledRole=CalledRole] [Empty(NoTReqs)] t ?TAddress@5 ?TSP@5
| | | | | [and(IsTCONind(TSP@5),IsCalledOf(TAddress@5,TSP@5))]
| | | | | [IsTReq(TSP@5)]
| | | | | [ne(TAddress@5,TAddress@1)]
| | | | | [IsTReq(TSP@5)] [730,746,779,788,800] ** rule B(2) **
```

2. Process TCEPSPOrdering (role: User Role): Local ordering of primitives at a calling endpoint

2.1 Labelled Symbolic Tree (depth = 7)

```
1 [role=CallingRole] t ?ta ?tcr [and(IsTCNreq(tcr),IsCallingOf(ta,tcr))] [744]
| 1 i (enable: exit !tcr ) [744]
| | 1 t ?ta ?tc2 [IsValidTCN2For(tc2,tc1)] [750]
| | | 1 [IsTEXOptionOf(x,tc2)] i (enable: exit !x:TEXOption) [751]
| | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758]
| | | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758]
| | | | | 2 [x=UseTEX] t ?ta ?tsp [IsTEX(tsp)] [761] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764] ==> continue
| | | | | 2 [x=UseTEX] t ?ta ?tsp [IsTEX(tsp)] [761]
| | | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758] ==> continue
| | | | | 2 t ?ta ?tsp [IsTEX(tsp)] [761] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764]
| | | | | 1 exit ** EXIT SUCCEED ** [764]
| | | | 2 [x=UseTEX] t ?ta ?tsp [IsTEX(tsp)] [761]
| | | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758]
| | | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758] ==> continue
| | | | | 2 t ?ta ?tsp [IsTEX(tsp)] [761] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764] ==> continue
| | | | | 2 t ?ta ?tsp [IsTEX(tsp)] [761]
| | | | | 1 t ?ta ?tsp [IsIDT(tsp)] [758] ==> continue
| | | | | 2 t ?ta ?tsp [IsTEX(tsp)] [761] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764] ==> continue
| | | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764]
| | | | | 1 exit ** EXIT SUCCEED ** [764]
| | | | 3 t ?ta ?tsp [IsIDIS(tsp)] [764]
| | | | | 1 exit ** EXIT SUCCEED ** [764]
| | | | 2 t ?ta ?tsp [IsIDIS(tsp)] [764]
| | | | | 1 exit ** EXIT SUCCEED ** [764]
| | | 2 t ?ta ?tsp [IsIDIS(tsp)] [764]
| | | | 1 exit ** EXIT SUCCEED ** [764]
| | 3 [role=CalledRole] exit ** EXIT SUCCEED ** [740]
```

2.2 Simplified Symbolic Tree

This is the resulting tree after the simplification rules discussed in the paper are applied.

Congruence rule 1 was applied twice, while rule 2 was applied once.

```
bh0 * 1 [CallingRole=CallingRole] t ?ta ?tcr[and(IsTCNreq(tcr),IsCallingOf(ta,tcr))] [744]
bh1 * | 1 t ?TAddress@ ?TSP@[IsValidTCN2For(TSP@[,])] [750]
bh2 * | | 1 [IsTEXOptionOf(TEXOption%2,TSP@0)] t ?TAddress@1 ?TSP@[IsIDT(TSP@1)] [758]
bh3 * | | | 1 t ?TAddress@2 ?TSP@[IsIDT(TSP@2)] [758] ==> again bh3
* | | | 2 [TEXOption%2=UseTEX] t ?TAddress@2 ?TSP@[IsTEX(TSP@2)] [761]
bh4 * | | | | 1 t ?TAddress@3 ?TSP@[IsIDT(TSP@3)] [758] ==> again bh4
* | | | | 2 t ?TAddress@3 ?TSP@[IsTEX(TSP@3)] [761] ==> again bh4
* | | | | 3 t ?TAddress@3 ?TSP@[IsIDIS(TSP@3)] [764]
bh5 * | | | | 1 exit ** EXIT SUCCEED ** [764]
* | | | | 3 t ?TAddress@2 ?TSP@[IsIDIS(TSP@2)] [764]
* | | | | 1 exit ** EXIT SUCCEED **
* | | 2 [IsTEXOptionOf(TEXOption%2,TSP@0)][TEXOption%2=UseTEX]
* | | | t ?TAddress@1 ?TSP@[IsTEX(TSP@1)][761] ==> again bh4
* | | | 3 [IsTEXOptionOf(TEXOption%2,TSP@0)] t ?TAddress@1 ?TSP@[IsIDIS(TSP@1)] [764]
* | | | | 1 exit ** EXIT SUCCEED **
* | 2 t ?TAddress@0 ?TSP@[IsIDIS(TSP@0)] [764]
* | | 1 exit ** EXIT SUCCEED **
```

```

776
777 process TCEPReq[t](ta:TAddress,role:TUserRole):noexit:= TReq[t]||GetCalledTId[t](ta,role) endproc
778
779 process TReq[t]:noexit:= t?ta:TAddress?tsp:TSP[IsTReq(tsp)]; TReq[t] endproc
780
781 process TCEPInd[t](ta:TAddress,role:TUserRole):noexit:= TInd[t]||GetCalledTId[t](ta,role) endproc
782
783 process TInd[t]:noexit:= t?ta:TAddress?tsp:TSP [IsTInd(tsp)]; TInd[t] endproc
784
785 process GetCalledTId[t](ta:TAddress,role:TUserRole):noexit:=
786   ( [role = CallingRole] -> exit(ta)
787     []
788     [role = CalledRole] -> t?tal:TAddress?tsp:TSP [tal ne ta] ; exit(tal)
789   ) >> accept ta:TAddress in ConstantTA[t](ta)
790 endproc
791
792 process TCReqToInd[t](rh:TSP):noexit:=
793   TSPEvent[t](rh) >> accept rh1:TSP in TCReqToInd[t](rh1)
794 endproc
795
796 process TSPEvent[t](rh:TSP):exit(TSP):=
797   [NonEmpty(rh)] ->
798   ( choice tspi:TSP [] [tspi IsIndicationOf rh] -> i;t?ta:TAddress!tspi;exit(NoTReqs))
799   []
800   [Empty(rh)] -> t ?ta:TAddress?tsp:TSP [IsTReq(tsp)] ; exit(tsp)
801 endproc
802
803 endspec (* SimplifiedTransportService *)

```

The numbers on the left-hand side are line numbers.

These numbers appear in the following trees between square brackets to indicate the action offers which cooperate to yield the actions shown.

The specification is a simplified formal description of the OSI Transport Service, based on [ISO2].

The simplifications relate to the end-to-end constraints:

- only one connection is provided,
- queues of requests, lost requests and provider-generated indications are not specified.

2 specification SimplifiedTransportService [t] : noexit

(* data part *)

```
721 behaviour TConnection[t]
722 where
723
724 process TConnection[t]:noexit:=          TCEPs[t] || TCEPAssociation[t] endproc
725
726 process TCEPs[t]:exit:=      TCEP[t](CallingRole) ||| TCEP[t](CalledRole) endproc
727
728 process TCEP[t](role:TSUserRole):exit:= TCEPAddress[t]||TCEPSOrdering[t](role) endproc
729
730 process TCEPAddress[t]:exit:=          t?ta:TAddress?tsp:TSP ; ConstantTA[t](ta)
731                                     [> exit          endproc
732
733 process ConstantTA[t](ta:TAddress):noexit:= t!ta?tsp:TSP; ConstantTA[t](ta) endproc
734
735 process TCEPSOrdering[t](role:TSUserRole):exit:=
736   TCEPConnect1[t] (role) >> accept tsp:TSP in
737   ( ( TCEPConnect2[t](tsp) >> accept x:TEXOption in TCEPDataTransfer[t] (x)
738   ) [> TCEPRelease[t] )
739   []
740   [role = CalledRole] -> exit
741 endproc
742
743 process TCEPConnect1[t](role:TSUserRole):exit(TSP):=
744   [role=CallingRole] -> t?ta:TAddress?tcr:TSP[IsTCNreq(tcr) and (ta IsCallingOf tcr)]; exit(tcr)
745   []
746   [role=CalledRole] -> t?ta:TAddress?tci:TSP[IsTCNind(tci) and (ta IsCalledOf tci)]; exit(tci)
747 endproc
748
749 process TCEPConnect2[t](tc1:TSP):exit(TEXOption):=
750   t ?ta:TAddress ?tc2:TSP [tc2 IsValidTCN2For tc1] ;
751   ( choice x:TEXOption [] [x IsTEXOptionOf tc2] -> exit (x) )
752 endproc
753
754 process TCEPDataTransfer[t](x:TEXOption):noexit:=
755   TCEPNormalDataTransfer[t] ||| [x = UseTEX] -> TCEPExpeditedDataTransfer[t]
756 endproc
757 process TCEPNormalDataTransfer[t]:noexit:=
758   t ?ta:TAddress ?tsp:TSP [IsIDI(tsp)] ; TCEPNormalDataTransfer[t] endproc
759
760 process TCEPExpeditedDataTransfer[t]:noexit:=
761   t ?ta:TAddress ?tsp:TSP [IsIEX(tsp)] ; TCEPExpeditedDataTransfer[t]
762 endproc
763
764 process TCEPRelease[t]:exit:=      t?ta:TAddress?tsp:TSP[IsIDIS(tsp)]; exit endproc
765
766 process TCEPAssociation[t]:noexit :=
767   t ?ta:TAddress ?tsp:TSP [IsTReq(tsp)] ;
768   ( TAssoc1[t] (ta,CallingRole,CalledRole,tsp)
769   |||
770   TAssoc1[t] (ta,CalledRole,CallingRole,NoTReqs) )
771 endproc
772
773 process TAssoc1[t](ta:TAddress,from,to:TSUserRole,rh:TSP):noexit:=
774   ( TCEPReq[t](ta,from) ||| TCEPInd[t](ta,to) ) || TReqToInd[t](rh)
775 endproc
```

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ANNEX. An Example: Transport Connection

We give an extended example showing some aspects of our method. Because of the length of the trees obtained, only certain sample sections may be shown.

1. Specification

5. Conclusions and Future Work

The work presented in this paper is an effort towards a methodology for generating test suites from LOTOS specifications. We showed that useful execution trees can be generated by using existing interpreters and some basic simplification rules. More sophisticated heuristics could be added to the system. Similarly, other congruence rules could be added to the very basic list given in Section 3.2. As a further step, we are envisaging the use of theorem-proving methods to enhance the methods for detecting contradictions and equivalences.

Furthermore, this method should be related to the existing theory on generating test suites from LOTOS specification [EB][BSS]. And then, there is the problem of obtaining real test specifications with values, mentioned in Section 4.

Finally, while the main emphasis of this paper is on testing, it could be noted that trees are also interesting in verification. Therefore, application of similar techniques in verification appears to be possible.

Acknowledgment. The interpreter was written by J.P. Briand, M.C. Fehri, R. Guillemot, and M. Haj-Hussein. We are indebted to A. Obaid for many useful discussions, and we have also used some ideas due to H. Elgendy. This work was supported in part by the National Science and Engineering Research Council of Canada and Bell-Northern Research.

= $q||p$).

Internal events could be eliminated at the source by using modified inference rules, such as (in simplified form):

$exit -exit \rightarrow stop$

$A \gg B -a \rightarrow A' \gg B$ if $A -a \rightarrow A'$ and $name(a) \neq exit$

$A \gg B -a \rightarrow B'$ if $A -exit \rightarrow A'$ and $B -a \rightarrow B'$

Unfortunately, these rules do not work in some cases. For example, suppose that the *exit* statement appears as the first action as in the following example:

<pre> <i>process</i> <i>p[a,b]</i> := <i>exit</i> >> <i>a</i> ; <i>stop</i> [] <i>b</i> ; <i>stop</i> <i>endproc</i> </pre>

this behavior is equivalent to: $i ; a ; stop [] b ; stop$. By eliminating the internal event according to the rules above, we obtain the behavior:

$a ; stop [] b ; stop$.

This expression is not equivalent to the previous one. The semantics of the original specification give priority to *b* while the semantics of the second specification don't. The modified rules for the *exit* can only be used if constructs such as the one in the example do not occur in the specification, and this can be checked statically.

The question of the treatment of internal events due to enabling for testing purposes deserves further study.

4. Considering Values

After obtaining the simplified tree according to the procedure described above, execution sequences can be derived by the following steps:

- identifying, for every action in all remaining paths, all the values that can be accepted
- and then constructing the expanded tree.

Concerning the first step, this consists in replacing every symbolic action, containing or not a guard, by values. The set of values that can be accepted by an action is usually infinite.

Example:

For the LOTOS specification :

$g?x:Nat ; g?y:Nat [y \text{ gt } x] ; exit$
--

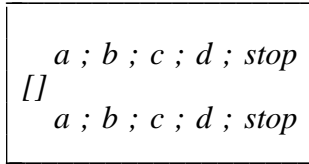
the SST obtained is:

```

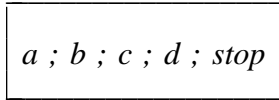
1 g?Nat@1
| 1 g?Nat@2 [Nat@2 gt Nat@1]
| | 1 exit

```

Values for *Nat@1* and *Nat@2* must be chosen before this sequence can be used as a test case. Possible values belong to the set of all pairs (x,y) of natural numbers such that $x < y$. Strategies for choosing such test values have been studied in the testing literature and will not be discussed in this paper [MY].

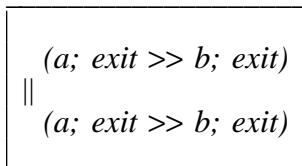


By scanning the tree according to this algorithm, it is simplified to:

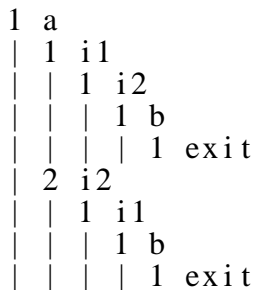


3.3. The Enable Operator.

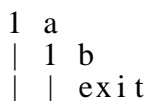
In our study of realistic examples, it soon became obvious that some method had to be found to manage the complexity generated by internal events due to enables. This can be seen by a study of Annex 3.2. For example, consider the following behavior expression:



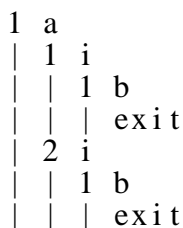
If we call $i1$ and $i2$ the internal actions resulting from the first and second enable respectively, the resulting **LST** shows both mutual orderings of these actions, i.e.



The tree of observable actions instead is simply



By applying the simplification rules discussed in **3.2.a)** we get:



This tree cannot be simplified further by the look-ahead mechanism because the behavior expression resulting from the execution of $i1$ is

$$(b; exit) \parallel (exit \gg b; exit)$$

while the behavior expression resulting from the execution of $i2$ is

$$(exit \gg b; exit) \parallel (b; exit)$$

and unfortunately these two behavior expressions are not textually identical.

The internal events will be completely eliminated by using the algorithm in 3.2.b) (and, of course, could be eliminated by the look-ahead mechanism if we informed it that $p \parallel q$

- *2* $B [] i;(B[]C)$ is simplified to $i;(B[]C)$
- *3* $B[]B$ is simplified to B

Note that the second rule is a stronger version of the well-known congruence $B[]i;B = i;B$. It is more useful than the latter, especially when disable is present.

The most obvious way to perform reduction by congruence rules is to generate the whole tree, to store it in memory, and then to scan it bottom-up to find places where congruence rules can be applied. However, memory can be saved if congruence rules are applied as far as possible, by using a look-ahead mechanism, already while the tree is being generated. The stored tree is then scanned bottom-up to further simplify it.

a) **Application of Congruence Rules While Building the Tree**

The interpreter is unable to directly apply rules 1 to 3 above. All it can do is to compute sets of possible next actions with resulting behavior expressions. Situations where these simplifications can be applied are detected by a "look-ahead" mechanism. Consider for example rule 2. When the interpreter finds that the set of possible "next actions" is of the form $N = \{a_1, \dots, a_m, b_1, \dots, b_n, i\}$, if the set of next behaviors is respectively $\{A_1, \dots, A_m, B_1, \dots, B_n, D\}$, the set of next actions for D is computed. If this set includes $\{b_1, \dots, b_n\}$, with next behaviors $\{B_1, \dots, B_n\}$ respectively, then the tree is simplified by using only the actions in $\{a_1, \dots, a_m, i\}$ and their successors. Again, two behavior expressions are considered to be the same only if they are the same character by character.

A corresponding criterion is used in testing for equivalence for rule 3.

These rules are applied recursively while possible. Therefore, an expression such as

$$\boxed{\begin{array}{l} a ; b ; (c ; d ; stop \\ \quad [] \\ \quad i ; c ; d ; stop) \end{array}}$$

is reduced to

$$\boxed{a ; b ; c ; d}$$

by applying first rule 2, and then rule 1. Annex 2.2. shows an extended example.

b) **Application of Congruence Rules on the Resulting Tree**

The tree resulting from the simplification process described above is stored in memory and further simplified by an algorithm that is able to detect other cases in which rules 1 to 3 can be applied. Consider for example the following behavior expression:

$$\boxed{\begin{array}{l} a ; b ; (c ; d ; stop \\ \quad [] \\ \quad i ; c ; d ; stop) \\ [] \\ a ; b ; c ; i ; d ; stop \end{array}}$$

By using the "look-ahead" mechanism, the following tree will be saved:

while in the first case, predicates involving the variable x would not be evaluated and would therefore all be considered to be true, unless they contain some contradiction independent of the value of x .

While equivalence of behavior expressions is an undecidable problem, more sophisticated criteria of behavior equivalence could be added to our system, also in consideration of the needs of the application. For example obviously behavior $a [] b$ can be considered to be identical to behavior $b [] a$. Furthermore, it is well-known that for testing purposes it may be appropriate to consider equivalent behaviors that cannot be considered to be equivalent from other points of view.

b) Ignoring Some Paths

In generating behavior trees for complex systems, it is normal that the user may wish to ignore certain paths. For example, this can happen for paths relating to error conditions, or for paths relating to the creation of several connections if it is wished to consider the case of one connection only. Such paths are usually guarded by internal actions. Our system allows one to specify that the entire subtree following a certain internal action be ignored.

3. The Treatment of Internal Events

A process in LOTOS is described in terms of its actions, which can be of two types: observable actions or internal actions. Internal actions occur in execution sequences either because they are specified explicitly (an i in the specification) or because they result from the dynamic behavior of the system (we call this implicit specification). This is the case for example when the enable (\gg) operator is used together with the *exit* statement.

Internal events, especially those due to enable operations, are a major cause of complexity in the symbolic tree. Hence the importance of eliminating them when possible.

3.1. Internal Events and Implementations

Internal actions introduce nondeterminism. Implementations may differ by the way they reduce this nondeterminism. Thus, for a given specification, one can obtain several valid implementations [BSS]. For instance, consider the following process:

```

process Connection[ConReq,DisInd,ConConf] : exit :=
    ConReq ; ( ConConf ; exit
              []
              i ; DisInd ; exit )
endproc

```

This is the connection phase of a protocol that always accepts a disconnection indication after a connection request, but may refuse the connection confirmation. The choice between these two alternatives is left to the implementation. Therefore, there are three possible implementations for this specification. One is the specification itself. The other two are:

- $ConReq ; (ConConf ; exit [] DisInd ; exit)$
- $ConReq ; (DisInd ; exit)$

The first alternative always offers *ConConf*, while the second never offers it.

Internal events designating implementation choices cannot be eliminated from the tree.

3.2. Simplification by Congruence Rules

In some cases, internal events can be removed by applying congruence rules. This removal does not in any way change the semantics of the specification. In this experiment, we implemented only the following rules:

$$*1* \quad a;i;B \quad \text{is simplified to} \quad a;B$$

```

1 in?Nat@1 [Nat @1 gt 3]
| 1 out?Nat@2 [Nat@2 gt Nat@1] [Nat@2 lt 3]
| | 1 exit EXIT
| 2 out?Nat@2 [Nat@2 gt Nat@1] [Nat@2 eq Nat@1]
| | 1 exit EXIT
2 in?1Nat@1 [Nat@1 le 3]
| 1 out!3 [3 lt 3]
| | 1 exit EXIT
| 2 out!3 [3 eq Nat@1]
| | 1 exit EXIT

```

- Branch 1.1 is pruned because of D(2) (it implies $3 < 3$).
- Branch 1.2 is pruned because of B(1).
- Branch 2.1 is pruned because of A (the predicate contains no variables and can be evaluated to false).

The SST is:

```

1 in?Nat@1 [Nat@1 gt 3] DEADLOCK
2 in?Nat@1 [Nat@1 le 3]
| 1 out!3 [3 eq Nat@1]
| | 1 exit EXIT

```

2.3 Towards a Limited Tree

Trees generated by this method are usually infinite. This is the normal case when recursion is involved. Two methods of dealing with infinite paths are detecting recursion, and ignoring some paths under user control.

a) Detecting Recursion

Recursion can be detected automatically at least in some cases. For example, a unique identifier can be associated with an occurrence of a behavior expression in a tree. Later occurrences of the same behavior expression or of an equivalent one in the same path are then replaced by the identifier preceded by the word "again". This can be done to a certain extent while building the tree, by comparing each behavior obtained against the ones obtained previously. The currently used comparison criterion is strict character-by-character identity. Although this may appear to be an overly simple criterion, we have found that it is useful in many cases. This is shown in Annex 2.2.

Example:

```

process P[a,d]:exit := a ; P[a,d]
                    []
                    d ; exit

```

The LST is:

```

1 a
| 1 a
| ...
| 2 d
| | 1 exit EXIT
2 d
| 1 exit EXIT

```

while the SST is:

```

bh0 1 a ==> again bh0
    2 d
    | 1 exit EXIT

```

Also, according to our criterion a behavior expression such as $P[a](x)$ is considered identical to $P[a](succ(x))$, while $P[a](0)$ would be considered different from $P[a](succ(0))$. This is because in the second case some predicates will be yielding different values for 0 and $succ(0)$,

The LST is:

```
1 g?x:Nat
| 1 i(enable:exit(x))
| | 1 g!y
```

while the SST is:

```
1 g?Nat@1
| 1 i(enable:exit(Nat@1))
| | 1 g!Nat@1
```

Example:

$$\text{choice } x:\text{Nat} \ [] \ g?y \ [y \text{ lt } x] ; \text{stop}$$

Value y bound at gate g must be less than value x chosen arbitrarily by the environment. Since a *choice* is not an action, we use the symbol % and represent x by $\text{Nat}\%1$.

The LST is: $1 \ g?y:\text{Nat} \ [y \text{ lt } x]$

The SST is: $1 \ g?\text{Nat}@1 \ [\ \text{Nat}@1 \ \text{lt} \ \text{Nat}\%1]$

2.2. Feasible Symbolic Trees

One may eliminate certain paths that are not feasible, by trying to evaluate symbolically guards and selection predicates [BJ]. Predicates that cannot be evaluated to false and are not in contradiction with others previously assumed to be true are assumed to be true. Each action is associated with a list of predicates, which gives all the constraints that must be satisfied for the action to be executed (these are the combined selection predicates of all action offers cooperating in the action). We call these *action predicates*. It is also associated with the list of predicates that occurred ahead of it on the same path, in guards or other predicates. We call these *path predicates*.

During the tree building process, an action is reduced to a *stop* if a contradiction is detected in its path predicates. It is checked in the following order whether:

- A) One of the action predicates can be evaluated to false.
- B) A contradiction can be detected in the action predicates
- C) A contradiction can be detected in the path predicates.
- D) A contradiction can be detected between path predicates and action predicates.

The detection of contradictions in the general case is of course an undecidable problem. Some heuristics are needed. Contradictions such as $(q(x) \text{ and } p(x))$, where $q(x) = \text{not}(p(x))$ appears in the list of axioms, are detected automatically. Upon finding a predicate such as this one, the system scans the list of axioms looking for such immediate contradictions. In specifications we have studied [ISO2], such cases are frequent.

In addition, our system allows the user to establish a data base of contradictions. A user-defined contradiction can involve several terms.

Example:

$$\begin{aligned} & (\text{in}?x:\text{Nat} \ [x \text{ gt } 3] ; \text{out}?y:\text{Nat} \ [y \text{ gt } x] ; \text{exit} \\ & \ [] \\ & \ \text{in}?x:\text{Nat} \ [x \text{ le } 3] ; \text{out}!3 ; \text{exit} \) \\ \parallel \\ & (\text{in}?x:\text{Nat}; (\text{out}?y \ [y \text{ lt } 3] ; \text{exit} \\ & \ [] \\ & \ \text{out}?y \ [y \text{ eq } x] ; \text{exit} \) \) \end{aligned}$$

Assume that the data base of contradictions contains:

- (1) $[x \text{ gt } y] \# [x \text{ eq } y]$
- (2) $[x \text{ gt } y] \& [y \text{ gt } z] \# [x \text{ lt } z]$

Before simplification, the tree is:

expression, one can find its behavior tree. In the absence of an environment, actions that depend on guards or selection predicates which cannot be evaluated because this involves the knowledge of values that have to be provided by the environment must be listed, together with their guards. Such trees will be called Labelled Symbolic Trees (**LSTs**).

Our LOTOS interpreter [GHL] is able to systematically generate LSTs for a given process up to given maximum lengths and widths. Such trees show all possible execution sequences for the entity specified. When the maximum specified length along a path is exceeded, this is indicated by closing the path with a "continue". Paths exceeding the specified width, instead, are simply ignored, but the user is informed of this (of course, the user must be aware of the fact that, if some paths are ignored, some of the procedures discussed in this paper may yield incorrect results). A realistic example is shown in Section 2.1 of the Annex.

1.3. Overview of The Method

Unfortunately, the practical usefulness of LSTs is greatly reduced by the many unfeasible, redundant, or uninteresting paths that they contain. This is especially true for specifications written in the constraint-oriented style, where each action is subject to a number of logical constraints originating from different processes. Heuristics can be used in order to obtain more useful trees by detecting and eliminating some such paths.

- The first step is to obtain a *Significant Symbolic Tree* (or **SST** for short), where input variable values are represented by symbols derived from the variable's name. Some unfeasible paths or actions are detected and removed by using techniques similar to "symbolic evaluation".
- Loops in behavior are identified.
- Some non-significant internal events are detected and removed.
- All the previous steps are executed dynamically as the tree is generated by the interpreter. In a final step, the stored tree is scanned in order to eliminate some remaining redundant internal events or duplicate paths.

Needless to say, the resulting tree is by no means optimal, in any possible meaning for this word. However it will usually be much more manageable than the original LST.

2. Obtaining a Significant Symbolic Tree

2.1 Contextual Symbolic Trees

Actions specified for a process may contain variables to be bound by the environment, values to be offered to the environment, and conditions on the variables and values (guards and selection predicates).

We use a symbolic representation for the variables which allows us to relate several occurrences of the same variable with different external names. A renaming scheme is used. Each occurrence of a variable is replaced by an identifier (a "symbol") which consists of:

- the variable's sort,
- a symbol which expresses how the value was bound, i.e. @ for a variable bound at a gate, and % for a variable bound in a *choice*, an *exit(any)*, or an initial process parameter.
- an identifier that shows the depth in the tree of the variable's first occurrence.
- a second identifier to distinguish different variables of the same sort and the same nesting level (if needed).

Example:

$g?x:Nat ; exit(x) \gg accept y:Nat in g!y ; stop$

The value of variable x is exported (by means of *exit* and enable operators) to become bound to variable y . By the renaming process, both variables get the identifier $Nat@1$, which stands for variable of sort Nat bound at level 1.

Derivation of Useful Execution Trees from LOTOS Specifications by Using an Interpreter

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A contribution towards the development of formal methodologies for testing protocol implementations is presented. We report on a system that is able to execute the specification of a protocol or service written in LOTOS and to derive an execution tree of the entity specified. Several heuristics are used in order to eliminate impossible or uninteresting execution paths. The tree obtained can then be used as a basis for the derivation of test suites.

1. Introduction

1.1. Generating Test Suites from Formal Specifications

Current test methods for protocols and services usually derive test suites manually from informal descriptions or semi-formal ones. The methodology towards which this paper intends to be a contribution assumes instead that the behavior of the entity to be tested has already been specified precisely in LOTOS [ISO1][BB] and derives test suites automatically or semi-automatically from this specification.

Several results have already been reported on generating test suites from formal specifications. Some recent references are [DEM][FL][SBMS][UR][URS][BSS]. Eertink and Brinksma [EB] have developed an algorithm, based on a formal theory, for deriving "canonical testers" for a specification written in a restricted version of "pure LOTOS" (i.e., LOTOS without data). The slant of our paper is more pragmatic. We deal with full LOTOS specifications, and we obtain execution sequences from specifications by using an existing tool, i.e. our LOTOS interpreter. As we shall see, the interpreter generates a great number of sequences that are either unfeasible, in the sense that they relate to logically impossible paths, or redundant, in the sense that they differ from the others only by the placement of nonrelevant internal events. Of course, eliminating all impossible paths and taking out all nonrelevant internal events involves unsolvable problems. Therefore, these execution sequences are simplified by using various heuristics in order to make them useful for testing purposes.

This technique does not constitute (yet) a methodology for the derivation of test suites. Apart from the several possible improvements to be discussed later, the remaining steps, which are the selection of test sequences and the formulation of test sequences in a test specification language, must still be done by hand using ad hoc methods.

1.2. Labelled Symbolic Trees (LSTs)

By LOTOS semantics, given a behavior expression B one can find the set of actions a and the set of resulting behavior expressions B' such that: $B \xrightarrow{a} B'$, meaning that process B can execute action a and transform into B' . In other words, given a behavior